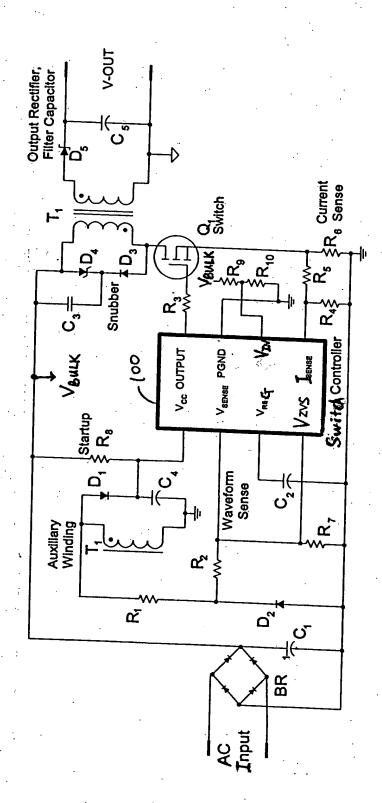
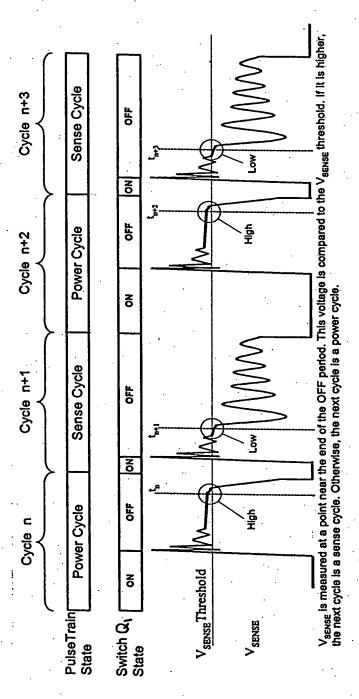
Express Mail No.: EV 334259897 US
Title: Power Converter Controller Having Event
Generator for Detection of Events and
Generation of Digital Error
Inventors: Mark D. Eason, et al.
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Title: Power Converter Controller Having Event Generator for Detection of Events and Generation of Digital Error Inventors: Mark D. Eason, et al. Atty. Docket No.: 23516-07980 Sheet 3 of 10 Output To Switch Q. 901 Pulse Train Generator 107 Event Bus 5 00 FIG. 2A 104 Defection Event Module のともる このと Vreg-comp BEST AVAILABLE COPY VREG + Vzvs Zero Voltage Switching VSEUSE Vsause . Vseuse Other Parametes

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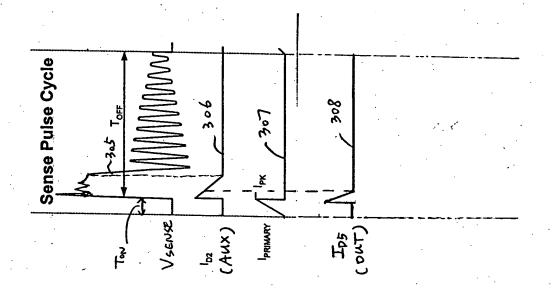
Inventors: Mark D. Eason, et al. Atty. Docket No.: 23516-07980 Sheet 4 of 10 Train Start Time Zus Optimized System. Resonance First Fall Transformer Reset time Stable Transformer Reset Pulse naing edge UREG Pulse Falling edge of thin Train "on time" Pulse Rising edge of train /SENSE

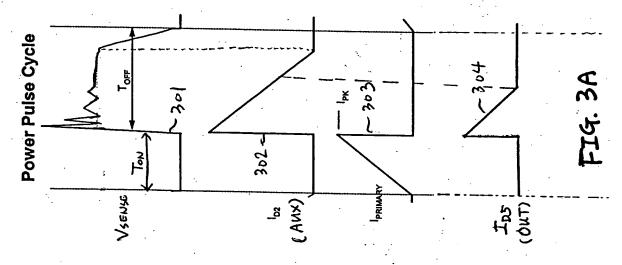
Express Mail No.: EV 334259897 US
Title: Power Converter Controller Having Event
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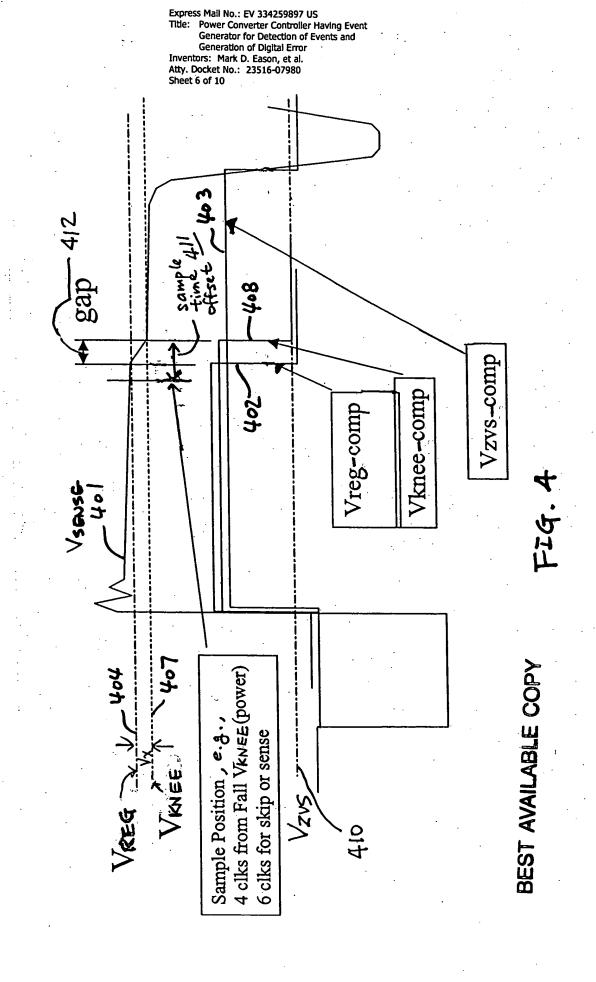
Generation of Digital Error

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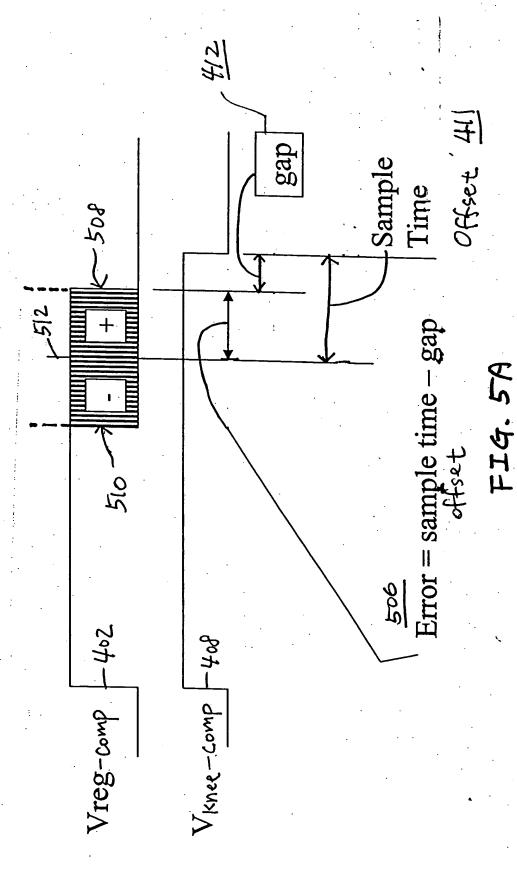






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Inventors: Mark D. Eason, et al.
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Sheet 8 of 10 Sample Position
4 clocks from falling VKNEE , 404 VREG 63 Gap=29 るのか 205 Vzvs-comp Vknee-comp Vreg-comp Error = -25 (neg = under-regulate) error = Sample offset - gap Vsense (un der regulated error =

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Inventors: Mark D. Eason, et al.
Atty. Docket No.: 23516-07980
Sheet 9 of 10 529 COUNT RESER 919 239 万五年 989/ 48 Vknee-omp 402 Vreg-comp-

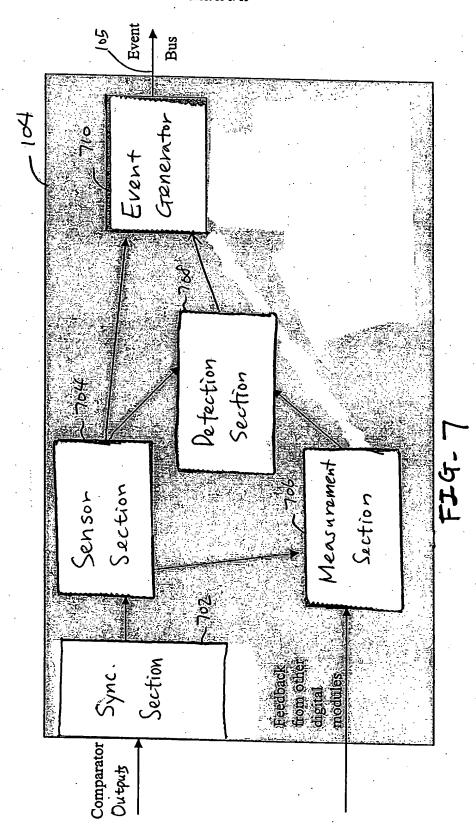
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412 Gap

403

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